

SREE VAHINI INSTITUTE OF SCIENCE AND TECHNOLOGY

(AN AUTONOMOUS INSTITUTION)

DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

Minutes of Board of Studies Online Meeting held on 02/04/2025, 11:00 AM

S. No	Name of the Member	Designation	Signature
1	Dr. K. Kiran Kumar	BOS-Chairman	
2	Dr. K. Venkata Reddy	Member (University Nominee)	Present-Online
3	Dr. D V S S Siva Sarma	Member (Subject Expert)	Present-Online
4	Dr. CH. Ravi Kumar,	Member (Subject Expert)	Present-Online
5	Mr. S. Prathap	Subject Expert (Industry)	Present-Online
6	Mr. N Malikarjuna Prasad	Expert (Outside)	Present-Online
7	Mr. B. Kutumba Rao	Alumni	Present-Online
8	Mrs. K. Ramya	Internal Member	
9	Mr. P. Uday Kumar	Internal Member	
10	Mrs. M. Hari Priya	Internal Member	
11	Mr. G. Chitti Babu	Internal Member	 ABSENT

Dr. K. Kiran Kumar, Head of the Department of Electrical and Electronics Engineering, Sree Vahini Institute of Science and Technology (Autonomous), chaired the Board of Studies Meeting.

The Chairman extended a warm welcome to the Hon'ble members of the Board of Studies and expressed his sincere gratitude to the University Nominee, Subject Experts, Industry Representative, Alumni Member, and

Internal Members for their valuable presence, constructive suggestions, and continuous support in enhancing the academic quality of the department.

The Chairman presented the agenda of the meeting, and the members deliberated on the agenda items in detail.

Discussion & Approval of the Board of Studies Meeting

Agenda Item: Discussion on the Academic Performance of Students for the Academic Year 2024–2025 (Odd Semester) – Result Analysis

The Chairman presented the academic performance report of the students for the Academic Year **2024–2025 (Odd Semester)**. The Board thoroughly reviewed the examination results and analyzed the overall pass percentage, subject-wise performance, and attainment of student outcomes for the following examinations:

- II B. Tech I Semester – Regular Examinations**



SREE VAHINI INSTITUTE OF SCIENCE AND TECHNOLOGY
Sai Vahini Nagar, NH-30, Tiruvuru-521235 :: NTR DT.:: A.P.:: INDIA
Affiliated to JNTUK Kakinada :: Approved by AICTE, New Delhi.

RESULTS ANALYSIS (After Revaluation)

Programme : B.Tech. (III Semester) (V'23) Regular

Exam Month & Year : Dec-2024

Year of Admission : 2023-24

Academic Year : 2024-2025

BRANCH	REGD.	APPEARED	PASSED	FAILED	PASS %
AIML	65	65	10	55	15.38
CE	6	6	0	6	0.00
CSE	134	134	53	81	39.55
CSE(DS)	63	63	24	39	38.10
ECE	118	117	49	68	41.88
EEE	32	32	13	19	40.63
ME	6	6	0	6	0.00
	424	423	149	274	35.22

**SREE VAHINI INSTITUTE OF SCIENCE AND TECHNOLOGY**

Sai Vahini Nagar, NH-30, Tiruvuru-521235 :: NTR DT.:: A.P.:: INDIA

Office of the Controller of Examinations**Programme : B.Tech (III Semester) Regular****Exam Month & Year : Dec-2024****Year of Admission : 2023-24****Academic Year : 2024-2025****ELECTRICAL AND ELECTRONICS ENGINEERING (After Revaluation)**

S.No.	COURSE CODE AND NAME	REGD.	APPEARED	PASSED	FAILED	PASS %
1	V231210221 - COMPLEX VARIABLES & NUMERICAL METHODS	32	32	22	10	68.75
2	V23121CC11 - UNIVERSAL HUMAN VALUES - UNDERSTANDING HARMON	32	32	27	5	84.38
3	V2312102L1 - ELECTROMAGNETIC FIE THEORY	32	32	22	10	68.75
4	V231210231 - ELECTRICAL CIRCUIT ANALYSIS - II	32	32	14	18	43.75
5	V231210232 - DC MACHINES & TRANSFORMERS	32	32	27	5	84.38
6	V231210261 - ELECTRICAL CIRCUIT ANALYSIS - II LAB	32	32	32	0	100.00
7	V231210262 - DC MACHINES & TRANSFORMERS LAB	32	32	32	0	100.00
8	V231210263 - DATA STRUCTURES LAB	32	32	32	0	100.00
9	V23121CCCC1 - ENVIRONMENTAL SCIENCE	32	32	32	0	100.00

I B. Tech I Semester – Regular Examinations**SREE VAHINI INSTITUTE OF SCIENCE AND TECHNOLOGY**

Sai Vahini Nagar, NH-30, Tiruvuru-521235 :: NTR DT.:: A.P.:: INDIA

Affiliated to JNTUK Kakinada :: Approved by AICTE, New Delhi.

RESULTS ANALYSIS (After Revaluation)**Programme : B.Tech. (I Semester) (V'23) Regular****Exam Month & Year : Dec-2024****Year of Admission : 2024-25****Academic Year : 2024-2025**

BRANCH	REGD.	APPEARED	PASSED	FAILED	PASS %
CSE	192	192	60	132	31.25
SE(AIM)	64	64	14	50	21.88
CSE(DS)	59	59	15	44	25.42
ECE	123	123	23	100	18.70
EEE	11	11	1	10	9.09
ME	1	1	0	1	0.00
	450	450	113	337	25.11

Programme : B.Tech (I Semester) Regular

Exam Month & Year : Dec 24 - Jan-25

Year of Admission : 2024-25

Academic Year : 2024-2025

ELECTRICAL AND ELECTRONICS ENGINEERING (After Revaluation)

S.No.	COURSE CODE AND NAME	REGD.	APPEARED	PASSED	FAILED	PASS %
1	V23111C521 - ENGINEERING PHYSICS	11	11	2	9	18.18
2	V23111CC22 - LINEAR ALGEBRA & CALCULUS	11	11	6	5	54.55
3	V23111C533 - BASIC ELECTRICAL & ELECTRONICS ENGINEERING	11	11	8	3	72.73
4	V23111C534 - ENGINEERING GRAPHICS	11	11	8	3	72.73
5	V23111CC35 - INTRODUCTION TO PROGRAMMING	11	11	6	5	54.55
6	V23111C566 - IT WORKSHOP	11	8	8	0	100.00
7	V23111C567 - ENGINEERING PHYSICS LAB	11	10	10	0	100.00
8	V23111C568 - ELECTRICAL & ELECTRONICS ENGINEERING	11	9	9	0	100.00
9	V23111C569 - COMPUTER PROGRAMMING LAB	11	8	8	0	100.00
10	V23111C5CA - NSS/NCC/SCOUTS & GUIDES/COMMUNITY SERVICE	11	11	10	1	90.91

I B. Tech II Semester – Supplementary Examinations



SREE VAHINI INSTITUTE OF SCIENCE AND TECHNOLOGY

Sai Vahini Nagar, NH-30, Tiruvuru-521235 :: NTR DT.:: A.P.:: INDIA

Affiliated to JNTUK Kakinada :: Approved by AICTE, New Delhi.

RESULTS ANALYSIS (After Revaluation)

Programme : B.Tech. (II Semester) (V23) Supplementa

Exam Month & Year : Dec-2024

Year of Admission :

Academic Year : 2024-2025

BRANCH	REGD.	APPEARED	PASSED	FAILED	PASS %
AIML	51	51	32	19	62.75
CSE	74	74	39	35	52.70
CSE(DS)	45	44	37	7	84.09
ECE	26	26	23	3	88.46
EEE	3	3	2	1	66.67
	199	198	133	65	67.17

Programme	: B.Tech (II Semester) Supplementary	Exam Month & Year	: Dec-2024			
Year of Admission	:	Academic Year	: 2024-2025			
ELECTRICAL AND ELECTRONICS ENGINEERING (After Revaluation)						
S.No.	COURSE CODE AND NAME	REGD.	APPEARED	PASSED	FAILED	PASS %
1	V23112C511 - COMMUNICATIVE ENGL	1	1	1	0	100.00
2	V23112CC22 - DIFFERENTIAL EQUATIONS & VECTOR CALCULUS	3	3	3	0	100.00
3	V23112C532 - BASIC CIVIL & MECHANICAL ENGINEERING	1	1	1	0	100.00
4	V231120231 - ELECTRICAL CIRCUITS -	1	1	1	0	100.00

• **I B. Tech I Semester – Supplementary Examinations**



SREE VAHINI INSTITUTE OF SCIENCE AND TECHNOLOGY
 Sai Vahini Nagar, NH-30, Tiruvuru-521235 :: NTR DT.: A.P.: INDIA
 Affiliated to JNTUK Kakinada :: Approved by AICTE, New Delhi.

RESULTS ANALYSIS (After Revaluation)

Programme : B.Tech. (I Semester) (V23) Supplementary Exam Month & Year : Dec-2024
 Year of Admission : 2024-25 Academic Year : 2024-2025

BRANCH	REGD.	APPEARED	PASSED	FAILED	PASS %
AIML	23	23	5	18	21.74
CSE	10	10	3	7	30.00
CSE(DS)	15	15	10	5	66.67
ECE	22	21	11	10	52.38
EEE	2	2	2	0	100.00
	72	71	31	40	43.66

**SREE VAHINI INSTITUTE OF SCIENCE AND TECHNOLOGY****Sai Vahini Nagar, NH-30, Tiruvuru-521235 :: NTR DT.:: A.P.:: INDIA****Office of the Controller of Examinations****Programme : B.Tech (I Semester) Supplementary****Exam Month & Year : Dec-2024****Year of Admission : 2024-25****Academic Year : 2024-2025****ELECTRICAL AND ELECTRONICS ENGINEERING (After Revaluation)**

S.No.	COURSE CODE AND NAME	REGD.	APPEARED	PASSED	FAILED	PASS %
1	V23111CC22 - LINEAR ALGEBRA & CALCULUS	2	2	2	0	100.00

The result analysis covered the overall pass percentages, subject-wise performance, department-wise analysis, and identification of courses requiring academic intervention. The Chairman highlighted the strengths observed in students' performance and the areas requiring improvement. The members discussed various measures to improve academic performance, including remedial classes, continuous assessment, mentoring, and outcome-based teaching strategies.

Decision:

The Board of Studies reviewed the academic performance of the students and appreciated the sincere efforts of the faculty members in achieving satisfactory academic outcomes. After detailed deliberations, the members recommended the following measures to further enhance student performance and academic excellence:

- Conduct remedial classes for academically weaker students.
- Organize tutorial and revision sessions prior to the Semester End Examinations.
- Strengthen the faculty mentoring and student monitoring system.
- Enhance continuous assessment, academic counselling, and regular student feedback mechanisms.
- Encourage students to participate in value-added courses, certification programmes, technical workshops, internships, and co-curricular

activities to improve their technical knowledge, professional skills, and employability.

Resolution:

The Board unanimously approved the above recommendations and resolved that the Department shall implement the suggested academic improvement measures and periodically review their effectiveness to ensure continuous enhancement of student performance and learning outcomes.

Suggestions Given by the Hon'ble Members

Dr. K. Venkata Reddy

- Appreciated the department for maintaining satisfactory academic standards and achieving commendable student performance.
- Suggested continuous monitoring and academic support for slow learners through an effective faculty mentoring system.

Dr. D. V. S. S. Siva Sarma

- Recommended conducting subject-wise result analysis after every examination to identify areas requiring academic intervention.
- Suggested organizing additional remedial coaching and tutorial classes for difficult subjects to improve student performance.

Dr. Ch. Ravi Kumar

- Advised strengthening students' analytical and problem-solving skills through practical laboratory exercises, coding practice, and hands-on learning activities.
- Encouraged the effective implementation of Outcome-Based Education (OBE) practices for continuous quality improvement and attainment of Programme Outcomes (POs) and Course Outcomes (COs).

Mr. S. Pratap (Industry Representative)

- Suggested enhancing students' communication skills, aptitude, programming, and technical competencies through value-added training programmes.
- Encouraged students to actively participate in internships, industrial visits, technical workshops, certification programmes, hackathons, and other industry-oriented activities to improve their practical knowledge and employability.

The Chairman thanked the Hon'ble members for their valuable suggestions and assured the Board that the recommendations would be incorporated into the department's academic action plan to further enhance the quality of teaching-learning and student performance.

Date: 02/04/2025.



BOS Chairperson
HEAD OF THE DEPARTMENT
DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING
SREE VAHINI INSTITUTE OF SCIENCE & TECHNOLOGY
TIRUVURU - 521 235, ANDHRA PRADESH